

FIG. 2

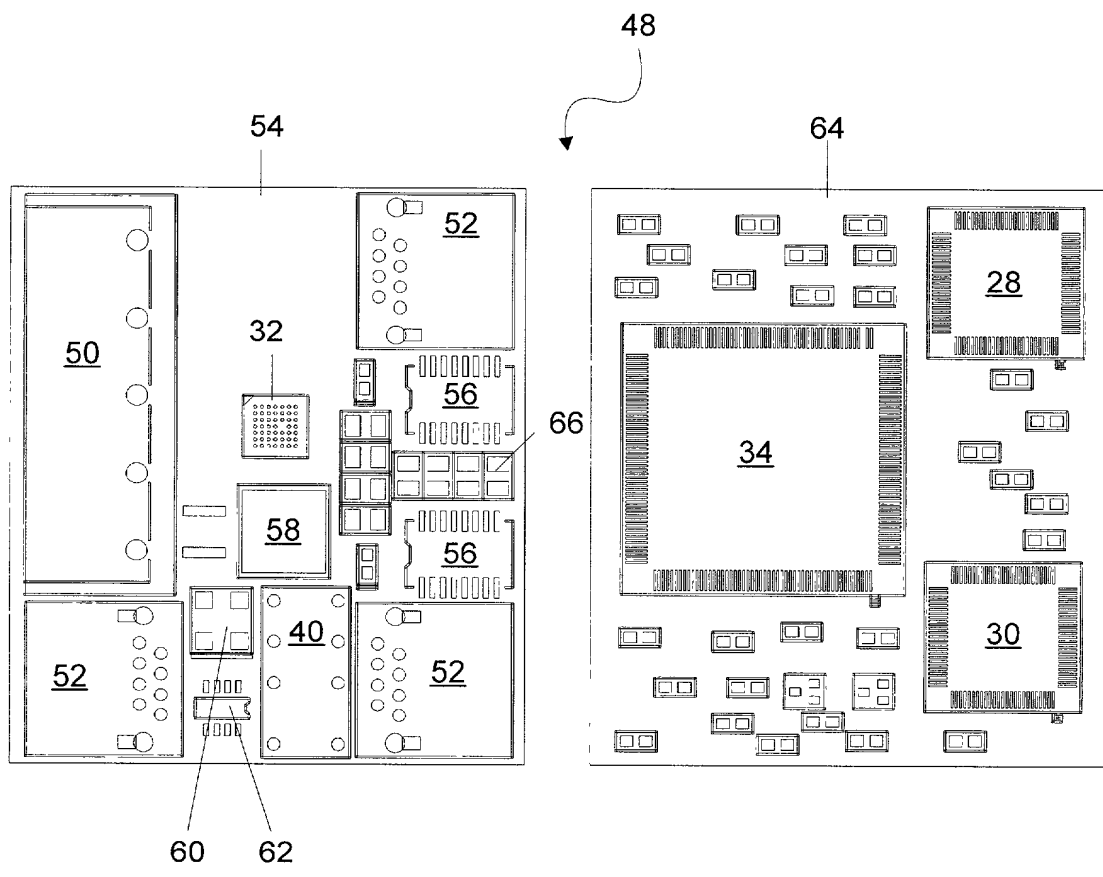


FIG. 3

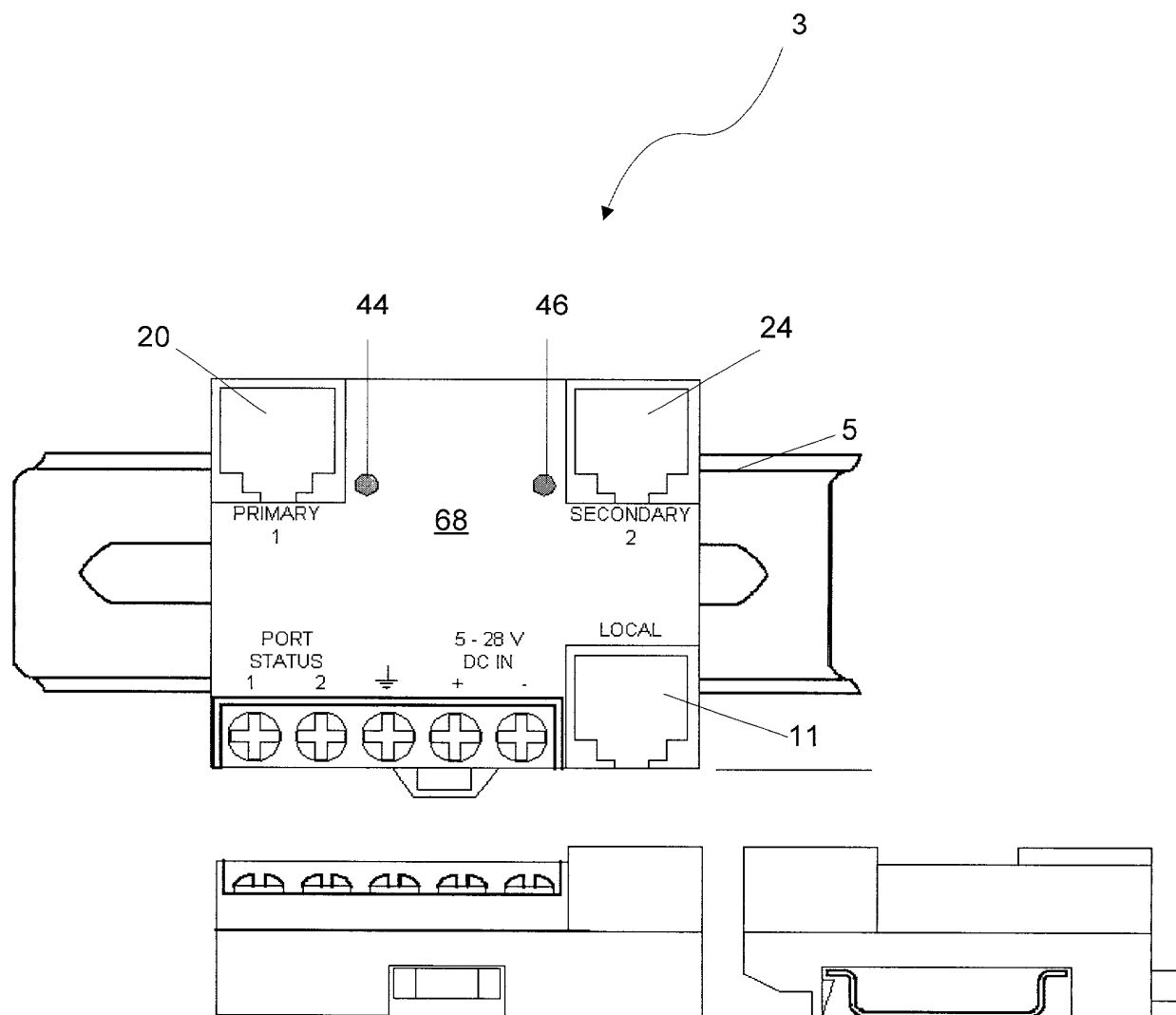


FIG. 4

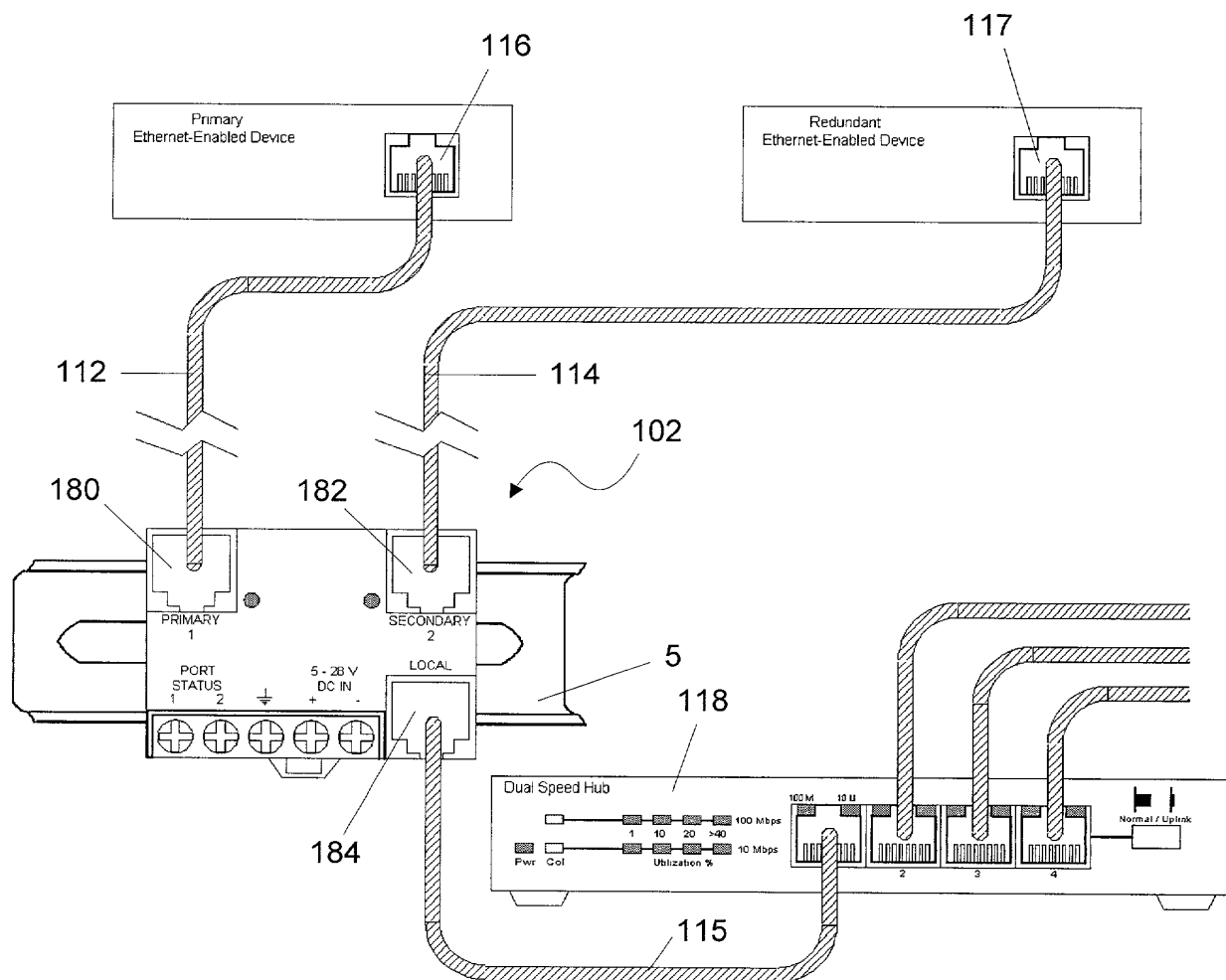


FIG. 5

FIG. 6 is a block diagram of a network switch system. The system includes a 10/100 PHY (128) connected to a CPLD (132). The CPLD (132) is connected to a Linear Vreg (134) which receives +5 - +24V DC In (136). The CPLD (132) is also connected to a Primary Selected LED (144) and a Secondary Selected LED (146). The system includes a Primary Port (180) and a Secondary Port (182). The Primary Port (180) is connected to the 10/100 PHY (128) and the CPLD (132). The Secondary Port (182) is connected to the 10/100 PHY (128) and the CPLD (132). The system also includes a switch (186) that connects the Primary Port (180) and the Secondary Port (182) to the 10/100 PHY (128).

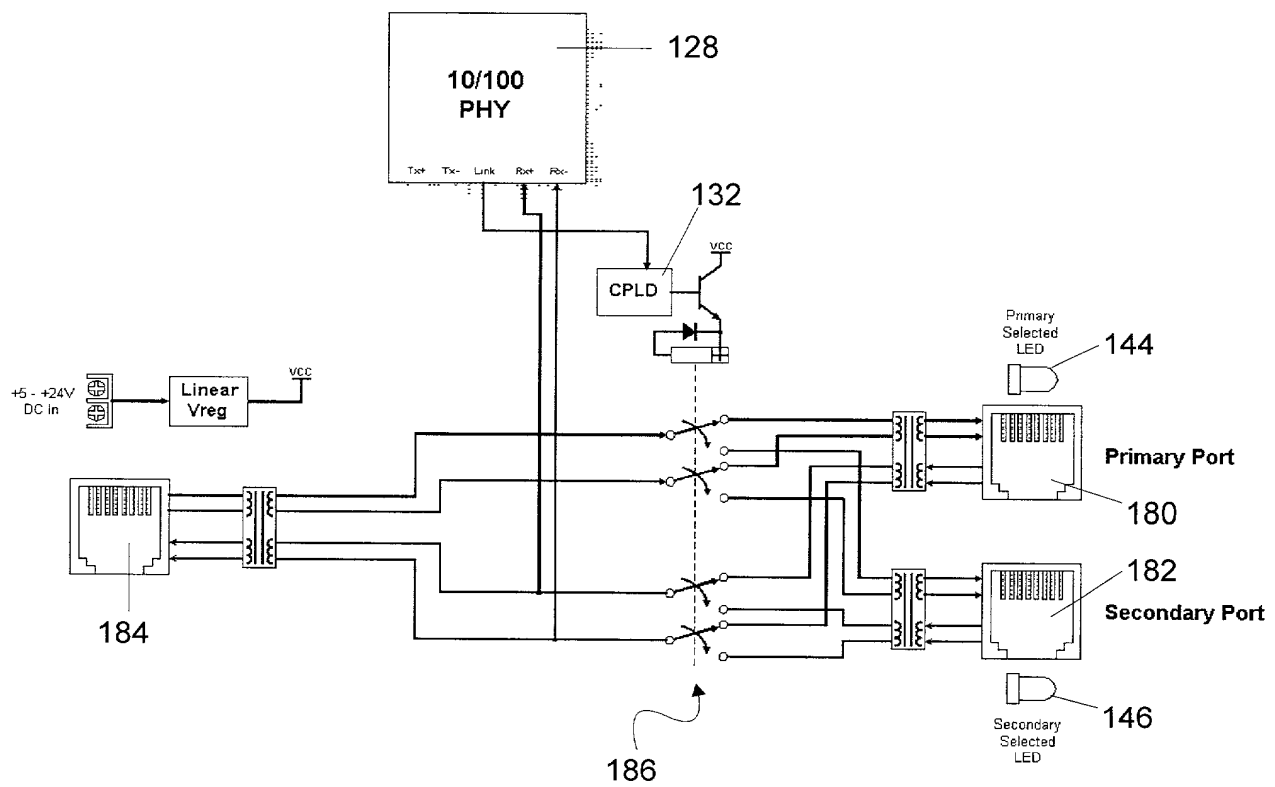


FIG. 6

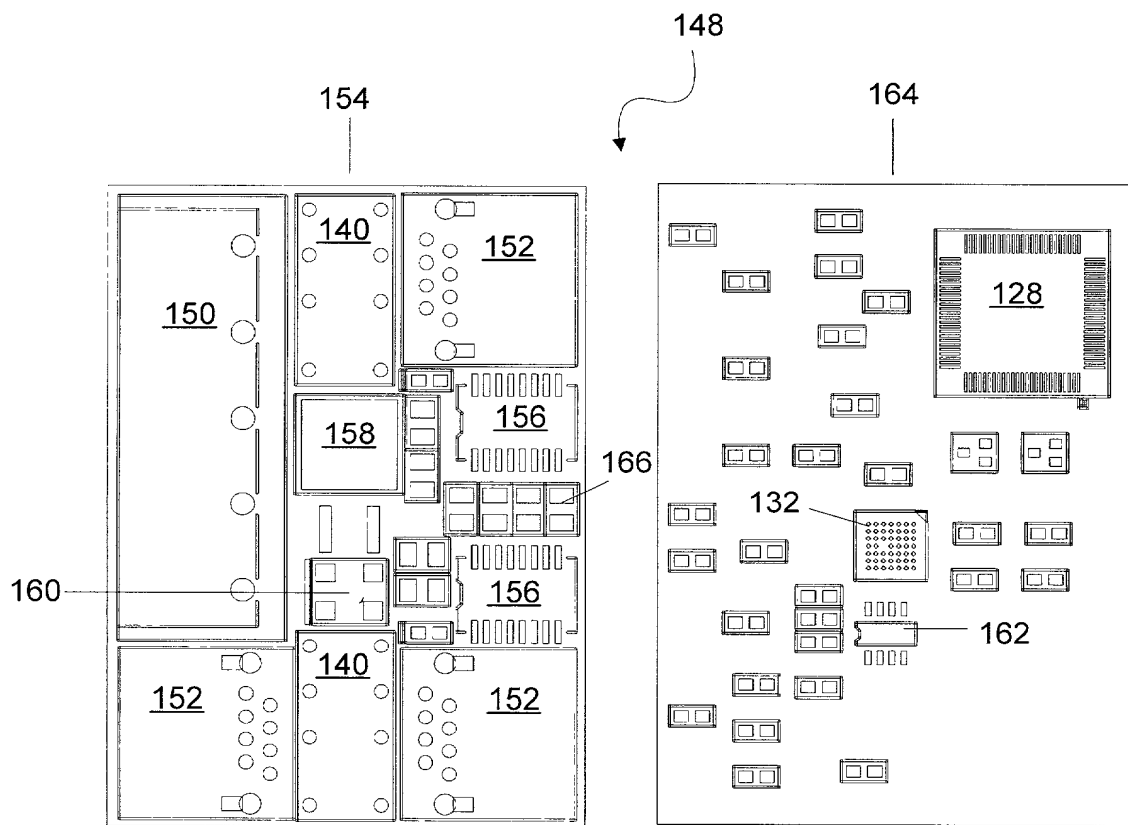


FIG. 7